

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	10002217
	Filing Date	2001-11-01
	First Named Inventor	Craig Nemecek
	Art Unit	2123
	Examiner Name	Jason Scott Proctor
	Attorney Docket Number	CD01207M

U.S.PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6910126	B1	2005-06-21	Mar et al.	Entire Document
	2	6825689	B1	2004-11-30	Snyder	Entire Document
	3	6798299	B1	2004-09-28	Mar et al.	Entire Document
	4	7023257	B1	2006-04-04	Sullam	Entire Document
	5	6967511	B1	2005-11-22	Sullam	Entire Document
	6	6892322	B1	2005-05-10	Snyder	Entire Document
	7	6823282	B1	2004-11-23	Snyder	Entire Document
	8	7127630	B1	2006-10-24	Snyder	Entire Document

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9	6611220	B1	2003-08-26	Snyder	Entire Document
10	7180342	B1	2007-02-20	Shuti et al.	Entire Document
11	6765407	B1	2004-07-20	Snyder	Entire Document
12	7386740	B1	2008-06-10	Kutz et al.	Entire Document
13	6952778	B1	2005-10-04	Snyder	Entire Document
14	7185162	B1	2007-02-27	Snyder	Entire Document
15	6854067	B1	2005-02-08	Kutz et al.	Entire Document
16	6614320	B1	2003-09-02	Sullam et al.	Entire Document
17	6868500	B1	2005-03-15	Kutz et al.	Entire Document
18	6859884	B1	2005-02-22	Sullam	Entire Document
19	7149316	B1	2006-12-12	Kutz et al.	Entire Document

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20	6981090	B1	2005-12-27	Kutz et al.	Entire Document
21	6603330	B1	2003-08-05	Snyder	Entire Document
22	6507214	B1	2003-01-14	Snyder	Entire Document
23	7005933	B1	2006-02-28	Shutt	Entire Document
24	6542025	B1	2003-04-01	Kutz et al.	Entire Document
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31	6563391	B1	2003-05-13	Mar	Entire Document
32	6608472	B1	2003-08-19	Kutz et al.	Entire Document
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	1	USPTO Non-Final Rejection for 10/238,966 (CD01250) dated 04/06/06; 8 pages	<input type="checkbox"/>
	2	"Micropower CMOS Temperature Sensor with Digital Output;" Bakker et al., IEEE Journal of Solid-State Circuits, 1996; 3 pages	<input type="checkbox"/>
	3	"WP 3.5: An Integrated Time Reference;" Blauschild, Digest of Technical Papers, 1994; 4 pages	<input type="checkbox"/>
	4	"An Analog PPL-Based Clock and Data Recovery Circuit with High Input Jitter Tolerance;" Sun, Reprinted from IEEE Journal of Solid-State Circuits, 1989; 4 pages	<input type="checkbox"/>
	5	Application Number 09/842,966 (CD00195): "Precision Crystal Oscillator Circuit Used in Microcontroller," Mar, 28 pages	<input type="checkbox"/>
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	1	5982229	B1	1999-11-09	Wong et al.	Entire Document
	2	6535946	B1	2003-03-18	Bryant et al.	Entire Document
	3	4827401	B1	1989-05-02	Hrustich et al.	Entire Document
	4	5594734	B1	1997-01-14	Worsley et al.	Entire Document
	5	5781747	B1	1998-07-14	Kametani	Entire Document
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10	4134073	B1	1979-01-01	MacGregor	Entire Document
11	5557762	B1	1996-09-17	Okuaki et al.	Entire Document
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13	5371860	B1	1994-12-06	Mura et al.	Entire Document
14	6575373	B1	2003-06-10	Nakano	Entire Document
15	5710906	B1	1998-01-20	Ghosh et al.	Entire Document
16	6294962	B1	2001-09-25	Mar	Entire Document
17	5590354	B1	1996-12-31	Klapproth et al.	Entire Document
18	7406674	B1	2001-10-24	Ogami et al.	Entire Document
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21	5552748	B1	1996-09-03	O'Shaughnessy	Entire Document
22	4689740	B1	1987-08-25	Moelands et al.	Entire Document
23	5559502	B1	1996-09-24	Schutte	Entire Document
24	5689196	B1	1997-11-18	Schutte	Entire Document
25	5670915	B1	1997-09-23	Cooper et al.	Entire Document
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39	4272760	B1	1981-06-09	Prazak et al.	Entire Document
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1	PCT/US99/09712	WO	A1	1999-02-25	Schutte	Entire Document	<input type="checkbox"/>
2	PCT/US96/17305	WO	A1	1996-06-06	Schutte	Entire Document	<input type="checkbox"/>
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	1	USPTO Notice of Allowance for Application Number 10/001,477 (CD1203M) dated 05/08/2009; 6 pages	<input type="checkbox"/>
	2	Application Number 09/943,062 (CD00228): "Apparatus and Method for Programmable Power Management in a Programmable Analog Circuit Block;" Mar; 46 pages	<input type="checkbox"/>
	3	Application Number 10/238,966 (CD01250M): "Method for Parameterizing a User Module;" Perrin; 41 pages	<input type="checkbox"/>
	4	USPTO Notice of Allowance for 10/324,455 (CD98087C2) dated 02/12/2004; 4 pages	<input type="checkbox"/>
	5	USPTO Non-Final Rejection for 10/324,455 (CD98087C2) dated 11/06/2003; 4 pages	<input type="checkbox"/>
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7	USPTO Notice of Allowance for 09/998,859 (CD01209M) dated 03/14/2005; 6 pages	<input type="checkbox"/>
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10	USPTO Non-Final Rejection for 09/998,859 (CD01209M) dated 05/28/2003; 6 pages	<input type="checkbox"/>
11	USPTO Non-Final Rejection for 09/998,859 (CD01209M) dated 05/15/2003; 6 pages	<input type="checkbox"/>
12	USPTO Notice of Allowance for 09/998,834 (CD01221M) dated 05/19/2005; 7 pages	<input type="checkbox"/>
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15	USPTO Final Rejection for 10/113,065 (CD01215M) dated 10/26/2005; 17 pages	<input type="checkbox"/>
16	USPTO Non-Final Rejection for 10/113,065 (CD01215M) dated 05/20/2005; 14 pages	<input type="checkbox"/>
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	3	6823497	B1	2004-11-23	Schubert et al.	Entire Document
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	5	6664978	B1	2003-12-16	Kekic et al.	Entire Document
	6	6601236	B1	2003-07-29	Curtis	Entire Document
	7	5479643	B1	1995-12-26	Bhaskar et al.	Entire Document
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9	6996799	B1	2006-02-07	Cismas et al.	Entire Document
10	5546433	B1	1996-08-13	Tran et al.	Entire Document
11	6542844	B1	2003-04-01	Hanna	Entire Document
12	6016563	B1	2000-01-18	Fleisher	Entire Document
13	6173419	B1	2001-01-09	Barnett	Entire Document
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20	6169383	B1	2001-01-02	Sabin et al.	Entire Document
21	5241492	B1	1993-08-31	Girardeau Jr.	Entire Document
22	6205574	B1	2001-03-20	Dellinger et al.	Entire Document

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	1	20020174134	A1	2002-11-21	Goykhman	Entire Document

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	1	1191423A2	EP	A1	2003-02-01	Garnell I Andreu	Entire Document	<input type="checkbox"/>
	2	404083405	JP	A1	1992-03-17	Yasue	Entire Document	<input type="checkbox"/>
	3	404095408	JP	A1	2002-03-27	Nishimaki	Entire Document	<input type="checkbox"/>

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4	405055842	JP	A1	1993-03-26	Kondo	Entire Document	<input type="checkbox"/>
5	368398	EP	A1	1990-05-01	Thommen	Entire Document	<input type="checkbox"/>
6	06021732	JP	A1	1994-01-28	Kadowaki, Tadao	Entire Document	<input type="checkbox"/>

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	1	Application Number 09/935,454 (CD00194): "Method and Apparatus for Local and Global Power Management in a Programmable Analog Circuit;" Mar; 51 pages	<input type="checkbox"/>
	2	Application Number 09/923,461 (CD00192): "Non-Interfering Multiply-Mac (Multiply Accumulate) Circuit;" Snyder; 25 pages	<input type="checkbox"/>
	3	Application Number 09/922,579 (CD00230): "A Method for a Efficient Supply to a Microcontroller;" Kutz; 37 pages	<input type="checkbox"/>
	4	Application Number 09/922,419 (CD00198): "A Power Supply Pump Circuit for a Microcontroller;" Kutz; 38 pages	<input type="checkbox"/>
	5	Application Number 09/912,768 (CD00200): "A Microcontroller having a Dual Mode Relax Oscillator that is Trimmable;" Shutt; 33 pages	<input type="checkbox"/>
	6	Application Number 09/893,048 (CD00231): "A Microcontroller having an On-Chip High Gain Amplifier;" Kutz; 22 pages	<input type="checkbox"/>

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7	Application Number 09/826,397 (CD00190): "Method and Circuit for Allowing a Microprocessor to Change its Operating Frequency on-the-fly;" Sullam; 24 pages	<input type="checkbox"/>
8	Application Number 09/887,955 (CD00197): "Novel Power on Reset Circuit for Microcontroller;" Kutz; 42 pages	<input type="checkbox"/>
9	Application Number 10/001,478 (CD01213M): "In-Circuit Emulator and POD Synchronized Boot;" Nemecek; 44 pages	<input type="checkbox"/>
10	Application Number 10/001,568 (CD01214M): "Combined In-Circuit Emulator and Programmer;" Nemecek; 47 pages	<input type="checkbox"/>
11	Application Number 10/011,214 (CD00191): "Method and Circuit for Synchronizing a Write Operation between an On-Chip Microprocessor and an On-Chip Programmable Analog Device Operating at Different Frequencies;" Sullam; 49 pages	<input type="checkbox"/>
12	Application Number 10/004,039 (CD01222M): "In-Circuit Emulator with Gatekeeper for Watchdog Timer;" Nemecek; 46 pages	<input type="checkbox"/>
13	Application Number 10/004,197 (CD01220M): "In-Circuit Emulator with Gatekeeper Based Halt Control;" Nemecek; 47 pages	<input type="checkbox"/>
14	Application Number 10/001,477 (CD01203M): "Breakpoint Control in an In-Circuit Emulation System;" Roe; 43 pages	<input type="checkbox"/>
15	Application Number 10/000,383 (CD00189): "System and Method of Providing a Programmable Clock Architecture for an Advanced Microcontroller;" Sullam; 34 pages	<input type="checkbox"/>
16	Application Number 09/887,923 (CD00229): "Novel Method and System for Interacting between a Processor and a Power on Reset to Dynamically Control Power States in a Microcontroller;" Kutz; 44 pages	<input type="checkbox"/>
17	Application Number 09/855,868 (CD00181): "Protecting Access to Microcontroller Memory Blocks;" Snyder; 26 pages	<input type="checkbox"/>

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18	Application Number 11/125,554 (CD00230C): "A Method for a Efficient Supply to a Microcontroller;" Kutz; 1 page	<input type="checkbox"/>
19	Application Number 10/272,231 (CD00170C): "Digital Configurable Macro Architecture;" Snyder; 36 pages	<input type="checkbox"/>
20	Application Number 09/977,111 (CD00202): "A Frequency Doubler Circuit with Trimmable Current Control; Shutt; 35 pages	<input type="checkbox"/>
21	Application Number 09/973,535 (CD00193): "Architecture for Decimation Algorithm;" Snyder; 26 pages	<input type="checkbox"/>
22	Application Number 09/972,133 (CD00178): "Method for Entering Circuit Test Mode;" Snyder; 30 pages	<input type="checkbox"/>
23	Application Number 09/972,003 (CD00176): "Test Architecture for Microcontroller Providing for a Serial Communication Interface;" Snyder; 32 pages	<input type="checkbox"/>
24	Application Number 09/957,084 (CD00201): "A Crystal-Less Oscillator with Trimmable Analog Current Control for Increased Stability;" Mar; 28 pages	<input type="checkbox"/>
25	Application Number 09/969,313 (CD00227): "Architecture for Synchronizing and Resetting Clock Signals Supplied to Multiple Analog Programmable System on a Chip Block;" Sullam; 50 pages	<input type="checkbox"/>
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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number	10002217
	Filing Date	2001-11-01
	First Named Inventor	Craig Nemecek
	Art Unit	2123
	Examiner Name	Jason Scott Proctor
	Attorney Docket Number	CD01207M

U.S. PATENTS

Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	7058921	B1	2006-06-06	Hwang et al.	Entire Document
	2	7024636	B1	2006-04-04	Weed	Entire Document
	3	7017145	B1	2006-03-21	Taylor	Entire Document
	4	7117485	B1	2006-10-03	Wilkinson et al.	Entire Document
	5	5371878	B1	1994-12-06	Coker	Entire Document
	6	6711731	B1	2004-03-23	Weiss, Mitchell	Entire Document
	7	6691301	B1	2004-02-10	Bowen	Entire Document
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9	3740588	B1	1973-06-01	Stratton et al.	Entire Document
10	6483343	B1	2002-11-19	Faith et al.	Entire Document
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U.S. PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20050240917	A1	2005-10-27	Wu, Yuh-Cheng	Entire Document

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Examiner Initial*	Cite No	Foreign Document Number ³	Country Code ²	Kind Code ⁴	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear	T ⁵
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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ⁵
	1	Application Number 10/803,030 (CD00169C): "Programmable Microcontrollable Architecture (Mixed Analog/Digital)," Snyder; 13 pages	<input type="checkbox"/>

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	1	6714066	B1	2004-03-30	Gorecki et al.	Entire Document
	2	6453175	B1	2002-09-17	Mizell et al.	Entire Document
	3	6185127	B1	2001-02-06	Myers et al.	Entire Document
	4	6141376	B1	2000-10-31	Shaw	Entire Document
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	8	6011407	B1	2000-01-04	New	Entire Document

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10	5396245	B1	1995-03-07	Rempfer	Entire Document
11	3940760	B1	1976-02-01	Brokaw	Entire Document
12	5963075	B1	1999-10-05	Hiragizawa	Entire Document
13	6150966	B1	2000-11-21	Eto et al.	Entire Document
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15	6211715	B1	2001-04-03	Terauchi	Entire Document
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21	5544311	B1	1996-08-06	Harenberg et al.	Entire Document
22	5664199	B1	1997-09-02	Kuwahara	Entire Document
23	6175914	B1	2001-01-16	Mann	Entire Document
24	6574590	B1	2003-06-03	Kershaw et al.	Entire Document
25	5479652	B1	1995-12-26	Dreyer et al.	Entire Document
26	6356960	B1	2002-03-12	Jones et al.	Entire Document
27	6101457	B1	2000-08-08	Barch et al.	Entire Document
28	6125416	B1	2000-09-26	Warren	Entire Document
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31	6041406	B1	2000-03-21	Mann	Entire Document
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34	6463488	B1	2002-10-08	San Juan	Entire Document
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Examiner Initial*	Cite No	Publication Number	Kind Code ¹	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20040205695	A1	2004-10-14	Fletcher, David	Entire Document

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	1	USPTO Notice of Allowance for Application Number 09/930,021 (CD00174) dated 11/26/2004; 4 pages	<input type="checkbox"/>
	2	USPTO Final Rejection for Application Number 09/930,021 (CD00174) dated 08/31/2004; 8 pages	<input type="checkbox"/>
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	4	USPTO Miscellaneous Action with SSP for Application Number 09/930,021 (CD00174) dated 10/01/2001; 1 page	<input type="checkbox"/>
	5	USPTO Notice of Allowance for Application Number 09/953,423 (CD00187) dated 07/12/2004; 6 pages	<input type="checkbox"/>
	6	USPTO Non-Final Rejection for Application Number 09/953,423 (CD00187) dated 02/06/2004; 5 pages	<input type="checkbox"/>
	7	USPTO Notice of Allowance for Application Number 09/957,084 (CD00201) dated 05/18/2004; 5 pages	<input type="checkbox"/>

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9	USPTO Non-Final Rejection for Application Number 09/957,084 (CD00201) dated 08/27/2003; 8 pages	<input type="checkbox"/>
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11	USPTO Non-Final Rejection for Application Number 09/957,084 (CD00201) dated 08/23/2002; 6 pages	<input type="checkbox"/>
12	USPTO Notice of Allowance for Application Number 09/969,313 (CD00227) dated 10/04/2005; 6 pages	<input type="checkbox"/>
13	USPTO Non-Final Rejection for Application Number 09/969,313 (CD00227) dated 05/06/2005; 9 pages	<input type="checkbox"/>
14	USPTO Requirement for Restriction/Election for Application Number 09/969,313 (CD00227) dated 03/18/2005; 6 pages	<input type="checkbox"/>
15	USPTO Notice of Allowance for Application Number 09/969,311 (CD00175) dated 03/01/2005; 4 pages	<input type="checkbox"/>
16	USPTO Non-Final Rejection for Application Number 09/969,311 (CD00175) dated 09/21/2004; 4 pages	<input type="checkbox"/>
17	USPTO Advisory Action for Application Number 09/969,311 (CD00175) dated 07/21/2003; 2 pages	<input type="checkbox"/>
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19	USPTO Non-Final Rejection for Application Number 09/969,311 (CD00175) dated 11/06/2002; 8 pages	<input type="checkbox"/>
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21	USPTO Non-Final Rejection for Application Number 09/972,319 (CD00179) dated 09/16/2004; 9 pages	<input type="checkbox"/>
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23	USPTO Requirement for Restriction/Election for Application Number 09/972,003 (CD00176) dated 05/06/2004; 4 pages	<input type="checkbox"/>
24	USPTO Non-Final Rejection for Application Number 09/972,003 (CD00176) dated 02/02/2004; 10 pages	<input type="checkbox"/>
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27	USPTO Final Rejection for Application Number 09/972,133 (CD00178) dated 03/30/2006; 8 pages	<input type="checkbox"/>
28	USPTO Non-Final Rejection for Application Number 09/972,133 (CD00178) dated 11/25/2005; 9 pages	<input type="checkbox"/>
29	USPTO Advisory Action for Application Number 09/972,133 (CD00178) dated 08/31/2005; 3 pages	<input type="checkbox"/>

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30	USPTO Final Rejection for Application Number 09/972,133 (CD00178) dated 06/29/2005; 10 pages	<input type="checkbox"/>
31	USPTO Non-Final Rejection for Application Number 09/972,133 (CD00178) dated 03/08/2005; 9 pages	<input type="checkbox"/>
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33	USPTO Non-Final Rejection for Application Number 09/975,104 (CD00183) dated 06/16/2006; 8 pages	<input type="checkbox"/>
34	USPTO Final Rejection for Application Number 09/975,104 (CD00183) dated 02/15/2006; 7 pages	<input type="checkbox"/>
35	USPTO Non-Final Rejection for Application Number 09/975,104 (CD00183) dated 08/16/2005; 6 pages	<input type="checkbox"/>
36	USPTO Non-Final Rejection for Application Number 09/975,104 (CD00183) dated 03/21/2005; 7 pages	<input type="checkbox"/>
37	USPTO Notice of Allowance for Application Number 09/975,030 (CD00185) dated 02/06/2006; 7 pages	<input type="checkbox"/>
38	USPTO Non-Final Rejection for Application Number 09/975,030 (CD00185) dated 10/20/2005; 7 pages	<input type="checkbox"/>
39	USPTO Non-Final Rejection for Application Number 09/975,030 (CD00185) dated 03/29/2005; 13 pages	<input type="checkbox"/>
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41	USPTO Notice of Allowance for Application Number 10/272,231 (CD00170C) dated 03/08/2004; 6 pages	<input type="checkbox"/>
42	USPTO Final Rejection for Application Number 10/272,231 (CD00170C) dated 11/05/2003; 5 pages	<input type="checkbox"/>
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44	USPTO Notice of Allowance for Application Number 11/125,554 (CD00230C) dated 02/07/2008; 4 pages	<input type="checkbox"/>
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47	USPTO Notice of Allowance for Application Number 09/855,868 (CD00181) dated 04/25/2005; 6 pages	<input type="checkbox"/>
48	USPTO Non-Final Rejection for Application Number 09/855,868 (CD00181) dated 08/26/2004; 7 pages	<input type="checkbox"/>
49	USPTO Notice of Allowance for Application Number 09/887,923 (CD00229) dated 09/27/2004; 5 pages	<input type="checkbox"/>
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	9	6396302	B1	2002-05-28	New et al.	Entire Document
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	20	6286127	B1	2001-09-04	King et al.	Entire Document
	21	6186960	B1	2000-12-26	Marneweck et al.	Entire Document
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U.S.PATENT APPLICATION PUBLICATIONS

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	2	20020059543	A1	2002-05-16	Cheng et al.	Entire Document
	3	20010010083	A1	2001-07-26	Satoh	Entire Document
	4	20020133794	A1	2002-09-19	Kanapathippillai et al.	Entire Document
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Application Number	10002217
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First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01207M

8	USPTO Final Rejection for Application Number 09/875,599 (CD00180) dated 08/25/2004; 17 pages	<input type="checkbox"/>
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Examiner Initial*	Cite No	Patent Number	Kind Code ¹	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	4608502	B1	1986-08-26	Dijkmans et al.	Entire Document
	2	6373954	B1	2002-04-16	Malcolm et al.	Entire Document
	3	5900780	B1	1999-05-04	Hirose et al.	Entire Document
	4	6850117	B1	2005-02-01	Weber et al.	Entire Document
	5	5973368	B1	1999-10-26	Pearce et al.	Entire Document
	6	5248843	B1	1993-09-28	Billings	Entire Document
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9	5564010	B1	1996-10-08	Henry et al.	Entire Document
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11	5455731	B1	1995-10-03	Parkinson	Entire Document
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14	6057705	B1	2000-05-02	Wojewoda et al.	Entire Document
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16	6141764	B1	2000-10-31	Ezell	Entire Document
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20	5734272	B1	1998-03-31	Belot et al.	Entire Document
21	4999519	B1	1991-03-12	Kitsukawa et al.	Entire Document
22	6031365	B1	2000-02-29	Sharpe-Gelsler	Entire Document
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31	6507215	B1	2003-01-14	Piasecki et al.	Entire Document
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	1	20020121679	A1	2002-10-29	Bazarjani et al.	Entire Document
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	4	Hong et al., "Hierarchical System Test by an IEEE 1149.5 MTM-Bus Slave-Module Interface Core," IEEE, 2000; 14 pages	<input type="checkbox"/>
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8	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," IEEE; 6 pages	<input type="checkbox"/>
9	Ghosh et al., "A Low Overhead Design for Testability and Test Generation Technique for Core-based Systems," IEEE, 1997; 10 pages	<input type="checkbox"/>
10	Zorian, "Test Requirements for Embedded Core-based Systems and IEEE P1500," IEEE, 1997; 9 pages	<input type="checkbox"/>
11	Zorian et al., "Testing Embedded-Core Based System Chips," IEEE, 1998; 14 pages	<input type="checkbox"/>
12	Papachristou et al., "Microprocessor Based Testing for Core-Based System on a Chip," IEEE, 1999; 6 pages	<input type="checkbox"/>
13	Maroufi et al., "Solving the I/O Bandwidth Problem in System on a Chip Testing," IEEE, 2000; 6 pages	<input type="checkbox"/>
14	Marsh, "Smart Tools Illuminate Deeply Embedded Systems," EDN, 2000; 7 pages	<input type="checkbox"/>
15	York et al., "On-chip Support Needed for SOC Debug," Electronic Engineering Times, 1999; 2 pages	<input type="checkbox"/>
16	Atmel Corporation: AT90SC Summary: "Secure Microcontrollers for Smart Cards," 1999; 7 pages	<input type="checkbox"/>
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19	Charles, Jr. et al., "Wirebonding: Reinventing the Process for MCMs," April 1998; IEEE 7th International Conference on Multichip Modules and High Density Packaging; 3 pages	<input type="checkbox"/>
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22	"Electronic Circuit Protector-Circuit Breaker," IBM Technical Disclosure Bulletin; Volume 36, Issue 8, August 1, 1993; 1 page	<input type="checkbox"/>
23	Application Number 09/989,816 (CD01165M): "Datasheet Browsing and Creation with Data-Driven Datasheet Tabs within a Microcontroller Design Tool;" Bartz et al.; 55 pages	<input type="checkbox"/>
24	Application Number 09/989,574 (CD01164M): "Method and System for using a Graphics user Interface for Programming an Electronic Device;" Bartz et al.; 43 pages	<input type="checkbox"/>
25	Application Number 10/113,065 (CD01215M): "System and Method for Automatically Matching Components in a Debugging System;" Nemecek et al.; 32 pages	<input type="checkbox"/>
26	Application Number 09/998,834 (CD01221M): "A System and a Method for Communication between and Ice and a Production Microcontroller while in a Halt State;" Nemecek; 33 pages	<input type="checkbox"/>
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	1	5161124	B1	1992-11-03	Love	Entire Document
	2	5686844	B1	1997-11-11	Hull et al.	Entire Document
	3	5669979	B1	1999-02-09	Bocchino	Entire Document
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	6	5245262	B1	1993-09-14	Moody et al.	Entire Document
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12	4250464	B1	1981-02-10	Schade, Jr.	Entire Document
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13	USPTO Final Rejection for Application Number 10/113,064 (CD01200M) dated 10/18/2005; 22 pages	<input type="checkbox"/>
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16	8154064	B1	2000-11-28	Proebsting, Robert J.	Entire Document
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	1	Application Number 11/850,260 (CD2007006): "Circuit and Method for Improving the Accuracy of a Crystal-less Oscillator Having Dual-Frequency Modes;" Wright et al.; 33 pages	<input type="checkbox"/>
	2	USPTO Non-Final Rejection for Application Number 11/850,260 (CD2007006) dated 03/06/2009; 7 pages	<input type="checkbox"/>
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6	USPTO Non-Final Rejection for Application Number 11/644,100 (CD05204) dated 04/14/2008; 10 pages	<input type="checkbox"/>
7	USPTO Notice of Allowance for Application Number 11/415,588 (CD05028) dated 03/11/2008; 6 pages	<input type="checkbox"/>
8	USPTO Advisory Action for Application Number 11/415,588 (CD05028) dated 01/14/2008; 3 pages	<input type="checkbox"/>
9	USPTO Final Rejection for Application Number 11/415,588 (CD05028) dated 10/19/2007; 8 pages	<input type="checkbox"/>
10	USPTO Non-Final Rejection for Application Number 11/415,588 (CD05028) dated 06/13/2007; 8 pages	<input type="checkbox"/>
11	USPTO Notice of Allowance for Application Number 12/218,404 (CD05028C1) dated 03/19/2009; 7 pages	<input type="checkbox"/>
12	USPTO Non-Final Rejection for Application Number 12/218,404 (CD05028C1) dated 09/30/2008; 8 pages	<input type="checkbox"/>
13	Application Number 11/644,100 (CD05204): "Differential-to-single ended signal converter circuit and method," Stiff; 33 pages	<input type="checkbox"/>
14	Application Number 11/415,588 (CD05028): "Voltage Controlled Oscillator Delay Cell and Method," Sivadasan; 24 pages	<input type="checkbox"/>
15	Application Number 12/218,404 (CD05028C1): "Voltage Controlled Oscillator Delay Cell and Method," Sivadasan; 23 pages	<input type="checkbox"/>
16	USPTO Notice of Allowance for Application Number 11/132,894 (CD04064) dated 04/26/2007; 4 pages	<input type="checkbox"/>

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23	USPTO Final Rejection for Application Number 11/322,044 (CD05198) dated 09/21/2007; 14 pages	<input type="checkbox"/>
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37	USPTO Notice of Allowance for Application Number 10/871,582 (CD03036) dated 03/30/2006; 6 pages	<input type="checkbox"/>
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Application Number	10002217
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Examiner Name	Jason Scott Proctor
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6	Application Number 09/943,149 (CD01085): "Method for Phase Locking in a Phase Lock Loop," Moyal et al.; 21 pages	<input type="checkbox"/>
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10	Application Number 09/047,595 (CD97115): "Roving Range Control to Limit Receive PLL Frequency of Operation," Scott; 35 pages	<input type="checkbox"/>
11	Application Number 09/216,460 (CD98099): "Circuit and Method for Controlling an Output of a Ring Oscillator," Abugharbieh et al.; 21 pages	<input type="checkbox"/>
12	Application Number 09/471,914 (CD99064): "Reference-Free Clock Generator and Data Recovery PLL," Dalmia et al.; 32 pages	<input type="checkbox"/>
13	Application Number 09/471,576 (CD99062): "Reference-Free Clock Generation and Data Recovery PLL," Dalmia; 30 pages	<input type="checkbox"/>
14	Application Number 10/083,442 (CD01264): "Method/Architecture for a Low Gain PLL with Wide Frequency Range," Meyers et al.; 28 pages	<input type="checkbox"/>
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16	Application Number 09/893,161 (CD00094): "Architecture of a PLL with Dynamic Frequency Control on a PLD," Moore; 32 pages	<input type="checkbox"/>

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17	Application Number 09/608,753 (CD00018): "PLL Lockout Watchdog," Wilson et al.; 24 pages	<input type="checkbox"/>
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19	Application Number 09/747,262 (CD00085): "Linearized Digital Phase-Locked Loop," Williams et al.; 9 pages	<input type="checkbox"/>
20	Application Number 09/981,446 (CD01102): "Oscillator Tuning Method," Hauck; 28 pages	<input type="checkbox"/>
21	Application Number 09/538,989 (CD98104): "Memory Based Phase Locked Loop," Krishnan; 27 pages	<input type="checkbox"/>
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	2	5867046	B1	1999-02-02	Sugasawa, Yasuo	Entire Document
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17	Application Number 09/989,808 (CD01169M): "Automatic generation of application program interfaces, source code, interrupts, and data sheets for microcontroller programming," Bartz et al.; 67 pages	<input type="checkbox"/>
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39	PCT International Search Report of the International Searching Authority for PCT/US05/28793, filed 08/12/2005, mailed 11/19/2007; 5 pages	<input type="checkbox"/>
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	1	Kory Hopkins, "Definition;" 01/16/1997; < http://www.cs.sfu.ca/cs/people/GradStudent.html >; 1 page	<input type="checkbox"/>
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9	USPTO Notice of Allowance for Application Number 09/475,879 (CD99047) dated 10/22/2004; 7 pages	<input type="checkbox"/>
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19	USPTO Non-Final Rejection for Application Number 10/137,497 (CD01197M) dated 11/05/2004; 17 pages	<input type="checkbox"/>
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	1	Application Number 11/273,708 (CD05141): "Capacitance Sensor Using Relaxation Oscillators," Snyder et al.; 33 pages	<input type="checkbox"/>
	2	USPTO Non-Final Rejection for Application Number 11/273,708 (CD05141) dated 03/19/2007; 16 pages	<input type="checkbox"/>
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